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Anodic-oxidising appts. for evaluating semiconductors - comprises mount to sample mount at top face, vessel to pour liq. through O-ring on sample and weight laid on vessel
Patent Assignee: MATSUSHITA ELECTRONICS CORP

Patent Family

Patent Number	Kind	Date	Application Number	Kind	Date	Week	Type
JP 6116796	A	19940426	JP 92267097	A	19921006	199421	B
JP 2947543	B2	19990913	JP 92267097	A	19921006	199943	

Priority Applications (Number Kind Date): JP 92267097 A (19921006)

Patent Details

Patent	Kind	Language	Page	Main IPC	Filing Notes
JP 6116796	A		3	C2SD-017/00	
JP 2947543	B2		3	C2SD-017/00	Previous Publ. patent JP 6116796

Abstract:

JP 6116796 A

Appts. comprises a sample mount to mount a sample at the top face, vessel to pour a liq. through an O-ring on the sample, and weight laid on the vessel. The weight may be sepd. from the vessel.

USE/ADVANTAGE - For evaluating semiconductors.

Dwg.1/5

Derwent World Patents Index

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Dialog® File Number 351 Accession Number 9894284

Sealing method for organic electroluminescence element - where 1st sealing layer is prep'd by vapour deposition and 2nd sealing layer is optical curing type resin coated on 1st layer then cured
Patent Assignee: MITSUI TOATSU CHEM INC

Patent Family

Patent Number	Kind	Date	Application Number	Kind	Date	Week	Type
JP 4267097	A	19920922	JP 9128219	A	19910222	199244	B

Priority Applications (Number Kind Date): JP 9128219 A (19910222)

Patent Details

Patent	Kind	Language	Page	Main IPC	Filing Notes
JP 4267097	A		6	H05B-033/04	

Abstract:

JP 4267097 A

Element is protected and sealed by at least 2 layers structure film, the first sealing layer from the element is formed by vapour deposition, the second layer of an optical curing type resin is coated on the first layer then cured.

USE/ADVANTAGE - Organic electro-luminescence element is for back light of LCD panel.
Deterioration of the luminous element is prevented.

In an example, an optical curing type resin was prep'd. by mixing 50 wt.% epoxy acrylate, 20 wt.% iso-bornyl-methacrylate, 30 wt.% tri-acryloyl-oxyethyl iso-cyanate and 3 wt.% for total of polymerisation initiator. A 20 nm thick luminous layer (30nm by 30 nm) of tris (8-hydroxy quinoline) aluminium complex was formed on a glass substrate by vapour deposition, 15 nm thick polymonochloro paraxylene film of the first layer was formed on it by vapour deposition, the photo curing type resin was coated on it as the second layer for sealing, cured by UV ray for 15 seconds in N2 to form 160 microns thick film.

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Basic Patent (Number,Kind,Date): JP 6116796 A2 940426

PATENT FAMILY:

Japan (JP)

Patent (Number,Kind,Date): JP 6116796 A2 940426

ANODIC OXIDATION DEVICE (English)

Patent Assignee: MATSUSHITA ELECTRONICS CORP

Author (Inventor): FUSE HARUHIDE; HIRAO TAKASHI

Priority (Number,Kind,Date): JP 92267097 A 921006

Applc (Number,Kind,Date): JP 92267097 A 921006

IPC: * C25D-017/00; C25D-011/32; H01L-021/316; H01L-021/473

Derwent WPI Acc No: * C 94-174200; C 94-174200

JAPIO Reference No: * 180401C000148; 180401C000148

Language of Document: Japanese

Patent (Number,Kind,Date): JP 2947543 B2 990913

Patent Assignee: MATSUSHITA ELECTRONICS CORP

Author (Inventor): FUSE HARUHIDE; HIRAO TAKASHI

Priority (Number,Kind,Date): JP 92267097 A 921006

Applic (Number,Kind,Date): JP 92267097 A 921006

IPC: * C25D-017/00; C25D-011/32; H01L-021/316; H01L-021/473

Language of Document: Japanese

INPADOC/Family and Legal Status

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Dialog® File Number 345 Accession Number 11764705

Basic Patent (Number,Kind,Date): JP 4267097 A2 19920922

PATENT FAMILY:

Japan (JP)

Patent (Number,Kind,Date): JP 4267097 A2 19920922

SEALING METHOD FOR ORGANIC LIGHT EMITTING ELEMENT (English)

Patent Assignee: MITSUI TOATSU CHEMICALS

Author (Inventor): TAKAHARA SHIGERU; OHASHI YUTAKA; FUKUDA NOBUHIRO;
KAMIO HIROYUKI

Priority (Number,Kind,Date): JP 9128219 A 19910222

Applic (Number,Kind,Date): JP 9128219 A 19910222

IPC: * H05B-033/04; C09K-011/02; C09K-011/06

CA Abstract No: ; 119(02)017681W

Derwent WPI Acc No: ; C 92-363257

JAPIO Reference No: ; 170057E000137

Language of Document: Japanese

Patent (Number,Kind,Date): JP 2793048 B2 19980903

Priority (Number,Kind,Date): JP 9128219 A 19910222

Applic (Number,Kind,Date): JP 9128219 A 19910222

IPC: * H05B-033/04

Language of Document: Japanese

INPADOC/Family and Legal Status

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ANODIC OXIDATION DEVICE

Publication Number: 06-116796 (JP 6116796 A), April 26, 1994

Inventors:

- FUSE HARUHIDE
- HIRAO TAKASHI

Applicants

- MATSUSHITA ELECTRON CORP (A Japanese Company or Corporation), JP (Japan)

Application Number: 04-267097 (JP 92267097) , October 06, 1992

International Class (IPC Edition 5):

- C25D-017/00
- C25D-011/32
- H01L-021/316
- H01L-021/473

JAPIO Class:

- 12.6 (METALS--- Surface Treatment)
- 41.3 (MATERIALS--- Semiconductors)
- 42.2 (ELECTRONICS--- Solid State Components)

Abstract:

PURPOSE: To enable the anodic oxidation of the surface of a sample by matching its height by using an O-ring.

CONSTITUTION: The sample 1 for anodic oxidation is disposed as a front surface and a container 3 for pouring liquid via the O-ring made of rubber is disposed thereon in a part of the sample 1. This container 3 is disposed in contact with the sample 1 and the leakage of the liquid is prevented by a weight 4. This weight 4 is formed to an annular shape. The weight 4 is formed easily separable from the container 3 for the liquid. The height of a sample base 2 is adjusted by the movement of a threaded part. Further, a metallic electrode 8 is taken out of the front surface of the sample 1. (From: *Patent Abstracts of Japan*, Section: C, Section No. 1231, Vol. 18, No. 401, Pg. 148, July 27, 1994)

JAPIO

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